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| Notice of Allowability | Application No. | Applicant(s) |
| | 09/651,944 | DRAPKIN ET AL. |
| | Examiner | Art Unit |
| | Hiep Nguyen | 2816 |

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTO-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. This communication is responsive to amendment filed on 04-25-06.
2. The allowed claim(s) is/are 32,34,38 and 41-44.
3. Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
 - a) All
 - b) Some*
 - c) None
 1. Certified copies of the priority documents have been received.
 2. Certified copies of the priority documents have been received in Application No. _____.
 3. Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

* Certified copies not received: _____.

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.
THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.

4. A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
5. CORRECTED DRAWINGS (as "replacement sheets") must be submitted.
 - (a) including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached
 - 1) hereto or 2) to Paper No./Mail Date _____.
 - (b) including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date _____.

Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
6. DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

Attachment(s)

1. Notice of References Cited (PTO-892)
2. Notice of Draftsperson's Patent Drawing Review (PTO-948)
3. Information Disclosure Statements (PTO-1449 or PTO/SB/08),
Paper No./Mail Date _____
4. Examiner's Comment Regarding Requirement for Deposit
of Biological Material
5. Notice of Informal Patent Application (PTO-152)
6. Interview Summary (PTO-413),
Paper No./Mail Date 05-03-06.
7. Examiner's Amendment/Comment
8. Examiner's Statement of Reasons for Allowance
9. Other _____.



TUAN T. LAM
PRIMARY EXAMINER

Examiner's Amendment

An examiner's amendment to the record appears below. Should the changes and /or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issued fee.

Authorization for this examiner' s amendment was given in a telephone interview with Attorney Jeffrey M. Glabicki (Reg. No. 42,584) on 05-03-06.

The application has been amended as follows:

IN THE CLAIMS

In claim 32

Line 14; change "further comprising" to --said charge pump further comprising--.

Line 22; change "a fifth node" to --the third node--.

In claim 34

Lines 4-5; change " a rate of change of voltage detection circuit coupled to said input for detecting a change in voltage of said input signal" to -- a voltage detection circuit coupled to said input for detecting a rate of change in voltage of said input signal--.

Lines 6-7; change "said detection circuit" to -- said voltage detection circuit--.

Lines 9-10; change "said detection circuit" to -- said voltage detection circuit--.

Line 11, change "said detection circuit" to -- said voltage detection circuit--.

Line 17, change "further comprising" to --said charge pump further comprising--.

In claim 43

Lines 4-5, change " a rate of change of voltage detection circuit coupled to said input for detecting a change in voltage of said input signal" to -- a voltage detection circuit coupled to said input for detecting a rate of change in voltage of said input signal--.

Lines 6-7, change "a correction circuit comprising a charge pump circuit coupled between said detection circuit" to -- a correction circuit coupled between said voltage detection circuit --.

Line 9, change "said detection circuit" to -- said voltage detection circuit--.

Line 11, change "wherein the high frequency circuit comprises" to -- wherein the correction circuit comprises--.

Reason for Allowance

In view of the amendment filed on 04-25-06, the prior art reference of records (US. 5,973,967) fails to teach or fairly suggest a method for reducing distortion of a signal applied to an input of a high frequency circuit having a parasitic capacitance between said input and ground, comprising the steps of: activating a charge pump for introducing a current to said parasitic capacitance to prevent said parasitic capacitance from drawing current from said input signal responsive to detection of a rate of change of a positive edge of said input signal by said device; said charge pump further comprising a first transistor, a second transistor, a third transistor, a fourth transistor, a current source and a capacitor as called for in claim 32 ; an apparatus for reducing distortion of a signal applied to an input of a high frequency circuit having a parasitic capacitance between said input and ground, comprising a detection circuit comprising a capacitor and a charge pump including first to fourth transistors and a current source as called for in claim 34; an apparatus for reducing distortion of a signal applied to an input of a high frequency circuit having a parasitic capacitance between said input and ground, comprising a capacitor and a correction circuit including first to fourth transistors and a current source as called for in claim 43. Therefore, claims 32, 34, 38 and 41-44 are presently allowed.

Conclusion

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Hiep Nguyen whose telephone number is (571) 272-1752. The examiner can normally be reached on Monday to Friday from 7:30am to 4:00pm.

Art Unit: 2816

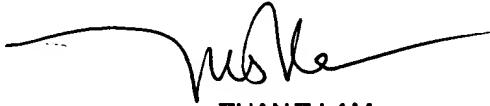
If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Timothy Callahan can be reached on (571) 272-1740. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Hiep Nguyen

05-04-06




TUAN T. LAM
PRIMARY EXAMINER